"A systematic approach for aircraft on-board system architecture within DEFAINE"

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17th MODPROD Workshop 7-8th Febr 2023, Linköping, Sweden

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Agenda

- DEFAINE overview
- On-Board System use case
 - Architecture generation
 - Time domain model automation
- Conclusion and future work





DEFAINE Objectives

- To reduce recurring cost in the design of aerospace systems and reduce the lead-time for design updates
- By enabling AI-powered front-loading
- Via a software framework that allows design engineers to perform large-scale design exploration studies
- Goals:
 - 10% cost reduction during design phase of aerospace systems
 - 50% lead-time reduction for design updates



DEFAINE goals: Front loading



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DEFAINE Overview



WP 2. Industrial use casesWP 3. Engineering services anddevelopment methodologiesWP 4. Workflows and data exchangestandardsWP 5. Design space exploration and AI

WP 1: Project Coordination WP 6: Dissemination and exploitation





Industrial use cases





Background & Use-Case Description

Backgorund:

- UAV overall design
 → early design stage
- Current development process based on point assessment / steady-state models
- Engineering tool/environment: PaceLab[®]
- Main Objective:
 - DSE for global vehicle configuration
 - Selection/Combination of on-board systems

Use Case Objective:

- Enable higher fidelity dynamic model of the on-board system, based on the design/vehicle information defined within the PaceLab[®]-based ACD process.
- By making use of a semi-automated KBE process containing:
 - a) system architecture design inf.,
 - b) subsystem sizing (domain specific), and
 - c) simulation tool specific information

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Background: Aircraft On-board Systems

- Power systems
- Specific domains: hydraulic, pneumatic, electric, (mechanic)
- System interdependencies
- Redundancy / Reliability
- Systematic split-up:
 - power generation (PGS)
 - power distribution (PDS)
 - power consumption (PCS)



Figure 3.3: Energy flow overview for a conventional power systems architecture

source: Liscouët-Hanke, A model-based methodology for integrated preliminary sizing and analysis of aircraft power system architecture, 2008.

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10

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On-Board Systems Architecture Design

- Creation of architecture is done manually
 - Time consuming
 - None added value
 - Prone to error
- Implementation of the different architectures is done manually component per component...
- Automate architecture creation and instantiation
 - KBE rules
 - Ease of implementation
 - Ease of modification



Current way of working



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Envisioned way of working





Approach

- Define logic for each type of system
 - Rules
 - Logical step
 - Input format to PaceSysArc
- Make usage of KBE environment offer by ParaPy
 - Ease of KBE app development
 - Ease of deployment: cloud



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Assystor app overview

Organization < ParaPy Overview	Info		
Users ParaPy Description Called a state of the state	ParaPy Description ParaPy SAVE	ParaPy On-board systems architecture modeler Atrant initiation Component selection Component Component selection Component Component selection	Aleron is spit Very One
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Current achievement architecture generation

- Automatic generation of FCS architecture with regards to:
 - Nb of control surfaces
 - Redundance level
 - Technology choices
- Time reduction
 - About 40 times quicker so far:
 - Still some manual work at the import in Pace SysArc
 - Enables quicker exploration of different architectures
- No full automation of architecture generation to design space exploration realised yet
 - Will lead to further reduce of time
 - Automated import will further reduce the time from architecture creation to usage with a factor of ~30





III. KBE-based process to enable higher-fidelity (dynamic) model in the early design stage



General Approach: Tool-specific Simulation-integrating KBE Approach



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Use-Case Specific Implementation



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Realized Simulation Model



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Conclusion

- A prototype to automate the on-board system architecture creation has been created and validated
 - Showing a time reduction of more than 40 times!
 - Further development is needed to cover more systems
- Integration to the time domain is under development
 - Extraction of flight condition specific information
 - Transformation of the steady state model to time domain
 - Enabling interoperability through standard (SSP, FMU/FMI)

